ABSTRACT OF THE DISCLOSURE

In an active matrix display device using an SRAM, the number of transistors configuring the SRAM circuit is large. Therefore, the transistors cannot be embedded in a pixel when a pixel area is small, otherwise an aperture ratio is reduced. In view of the foregoing, it is an object of the present invention to provide a display device without a refreshing operation and thus with small power consumption. According to the invention, a display device including a pixel which comprises a switching element and a nonvolatile memory element is provided. When a still image is displayed by utilizing a ferroelectric element as a nonvolatile memory element and storing a signal, writing is not required per frame. Further, as the ferroelectric element occupies a small area, a memory circuit can be incorporated without decreasing an aperture ratio.